ABSTRACT OF THE DISCLOSURE

In an image forming apparatus which includes a nonvolatile memory and is constituted by an engine section which forms an image and a controller section which transmits image data to the engine section through a video interface, synchronization of data transfer is established by a predetermined control signal that controls serial communication when data should be transferred from the controller section to the engine section to rewrite the nonvolatile memory, and the nonvolatile memory is rewritten by the data transferred in synchronism.

10